

January 2, 2001

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CPU Board Preparation FPGA now Installed by Manufacturer

General

The CPU Board (2/ROFNB 157 19/2) is now shipped with the necessary FPGA chip installed. Previously, the FPGA chip was shipped separately in the Sync Port Kit (NTMNB 101 107). The chip was inserted as part of on-site board preparation before installation.

The CPU Firmware will continue to be shipped separately and must be installed on the board after shipment from Ascom. The CPU also has board strap settings that must be set in order to configure the CPU for the correct air interface synchronization as well as frame timing. The insulation foil used to isolate the battery during storage must also be removed. See Figure 1.

Installation

For installation of the CPU board refer to Figure 1 and perform the following steps:

1. Use ESD precautions.
2. Insert the CPU Firmware, RYS 105 447 or RYS 105 657 in the proper socket.
3. Set the straps, shown in figure 1, in the correct position as described in the Technical Product Manual, Section 5, Paragraph 5.2.
4. Remove the insulation foil between the positive contact of the battery holder and the lithium battery.
5. Insert the CPU board gently in the card rack in board position 24. The cabinet in which the CPU resides automatically becomes cabinet 1.

FPGA Chip

The FPGA synchronization chip is necessary for operation of the CPU board. If for some reason you receive a CPU board that does not have the FPGA chip installed please contact Ascom Wireless Solutions Technical Support (1-877-71ASC0M). You may also Note Figure 2 and the Freeset Technical Product Manual R4A or higher for installation of the FPGA.

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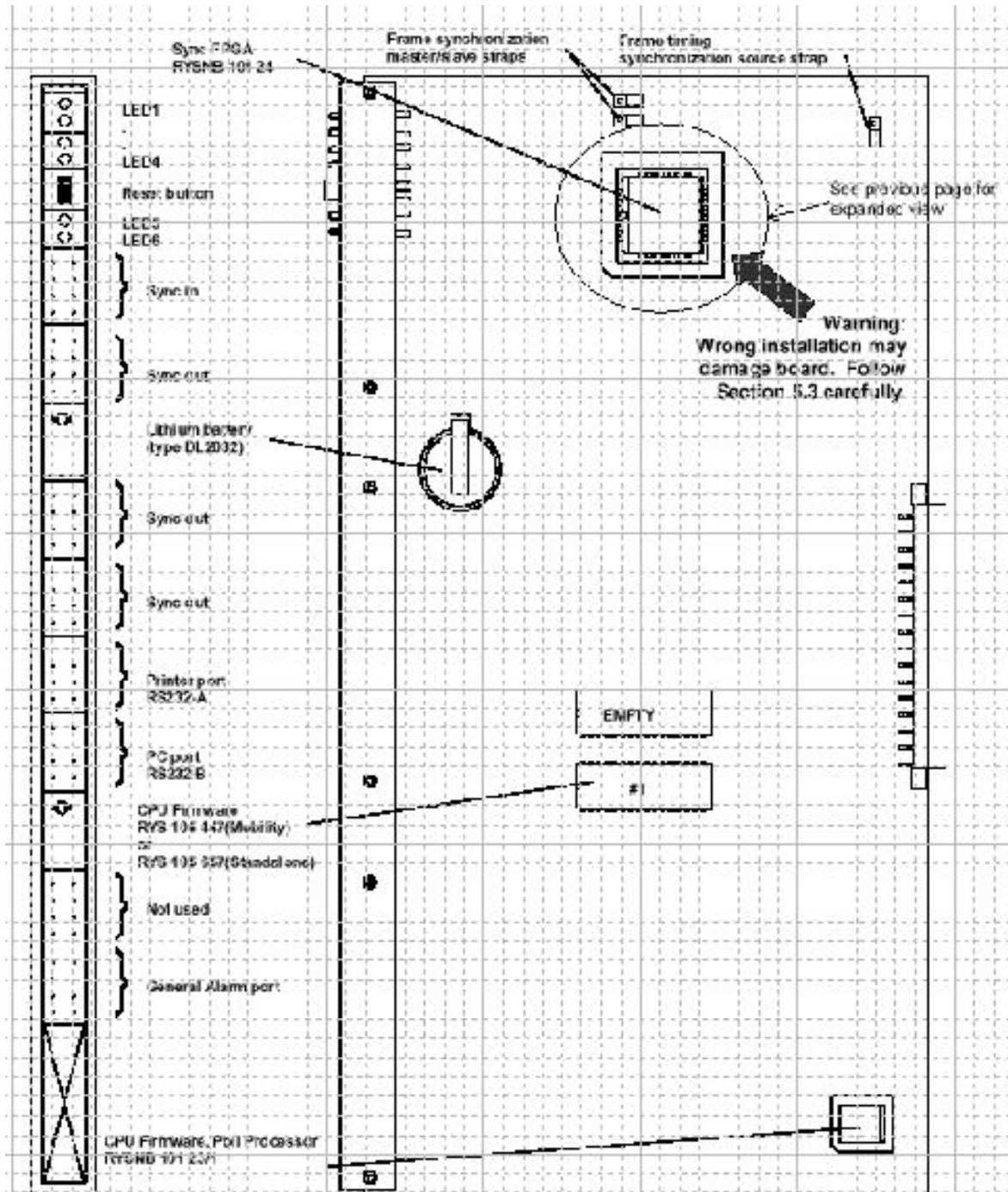


Fig. 5-3 CPU-2/ROFNB 157 19/2 cont.

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Figure 1

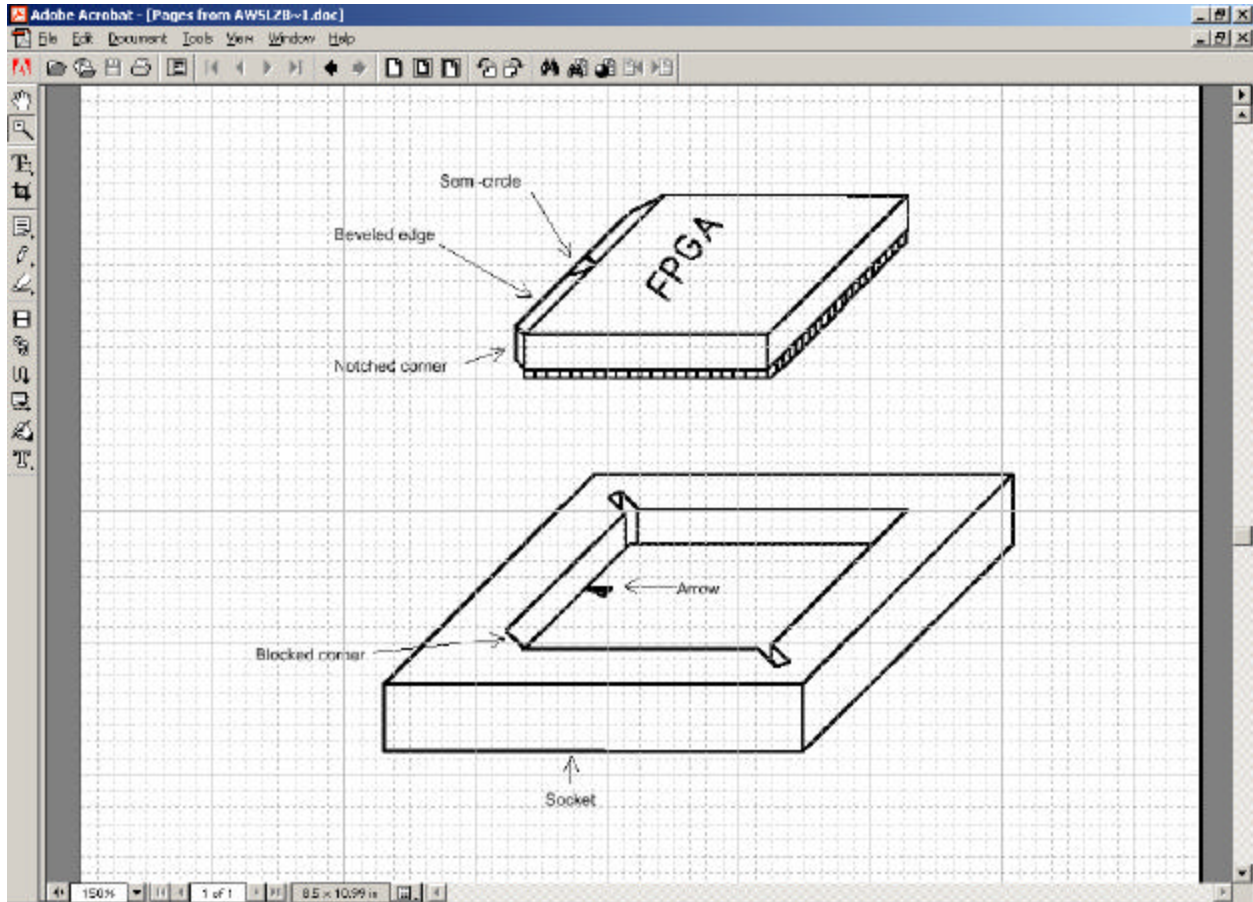


Figure 2